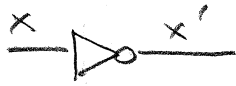


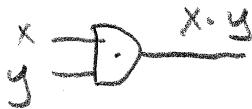
Gates & Circuits

Gates compute Boolean functions of their inputs.

Gate symbols



NOT



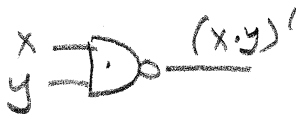
AND



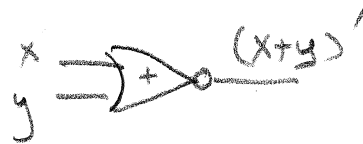
OR



XOR



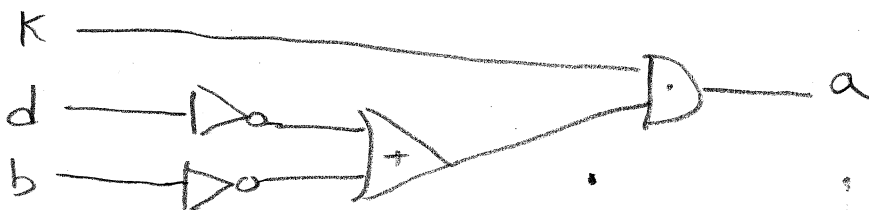
NAND



NOR

Wires may be inputs or outputs of a circuit or may connect the output of a gate to the input of another (or the same) gate.

Circuit for $a = k \cdot (d' + b')$



3 gate delays (The NOT gates compute in parallel.)