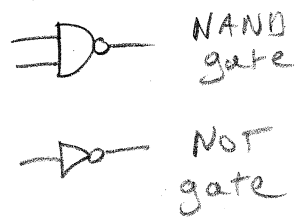
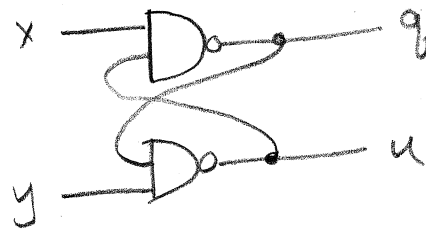


D Auglin
3/16/12

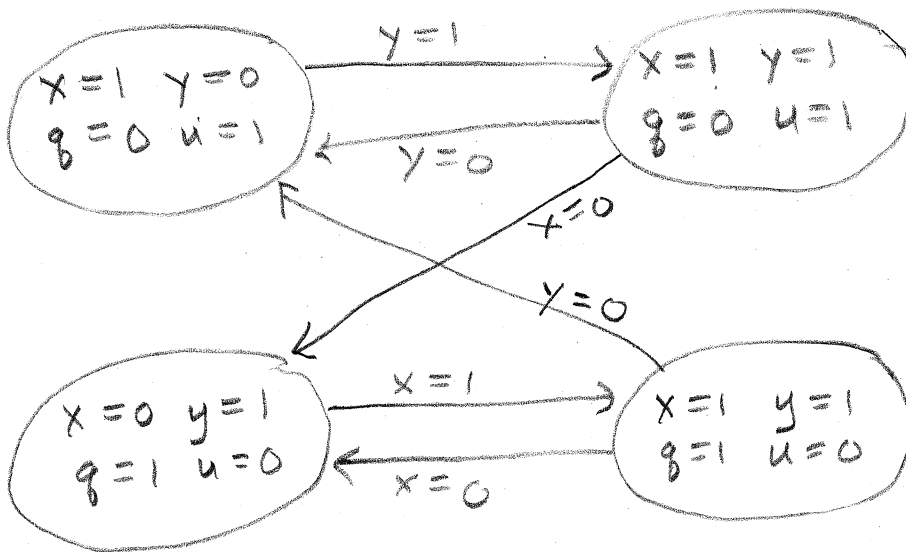
NAND latch



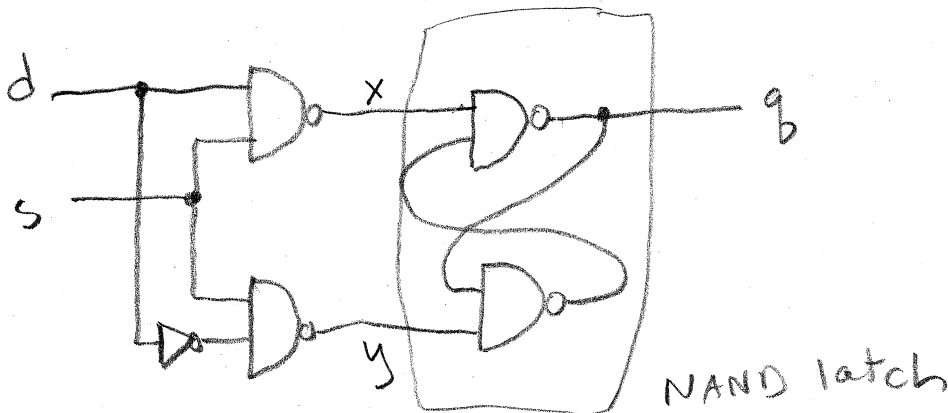
Stable states

x	y	q	u
0	0	1	1
1	0	0	1
0	1	1	0
1	1	0	1
1	1	1	0

} use these for 1-bit memory



state transitions



D flip flop