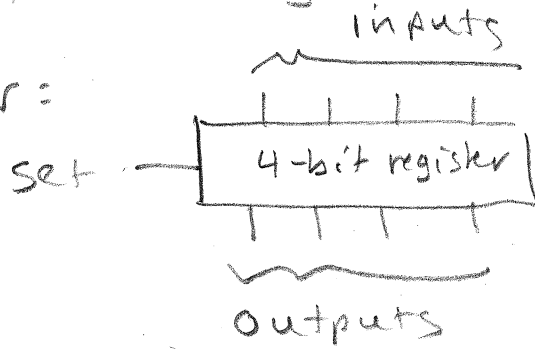


A Memory Write Design

- 4 4-bit registers, addressed 00, 01, 10, 11
- 1 4-bit MDR
- 1 2-bit MAR
- 1 read/write signal (write = 1)

A register:



(Read logic omitted)

