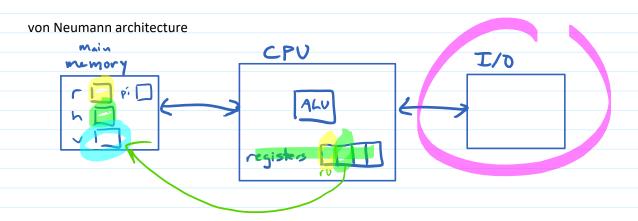
Source Code Translation





CPU: Qualcomm Snapdragon 845

CPU: Intel Core i5 6300U





CPU 1 (maybe Snapdragon 845) Machine Instructions

load r into register 1
multiply r1 by r1, store result in r1 (in R |
load h into r2
multiply r1 by r2, store result in r2 (in R)
load pi into r3
multiple r2 by r3, store result in r3
store r3 in v

load r into rl

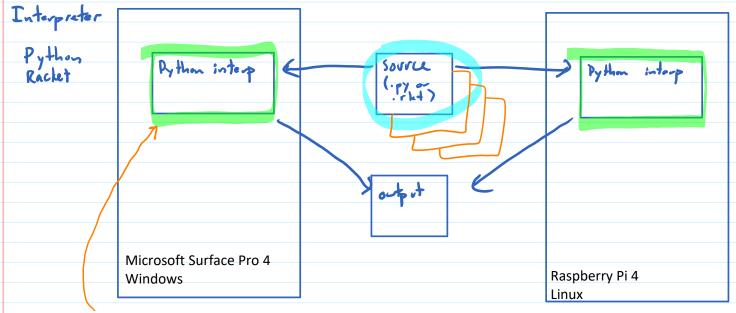
square rl

mult rl by h rahin rl

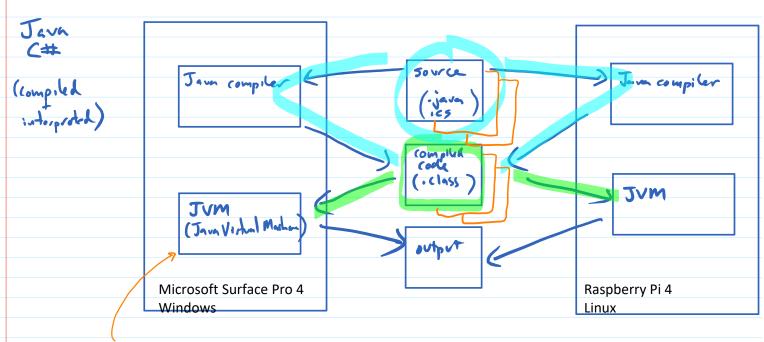
mult-pi rl

store rl in v

Interpreters vs Compilers



translation from high-level source code to CPU instructions happens here (likely not explicitly [as in by producing a list of instructions] but implicitly by choosing to run different parts of the interpreter, which is itself a list of machine instructions



like the interpreter above, except the input is machine instructions for the virtual machine inside the .class files instead of high-level source code

