

Source Code Translation

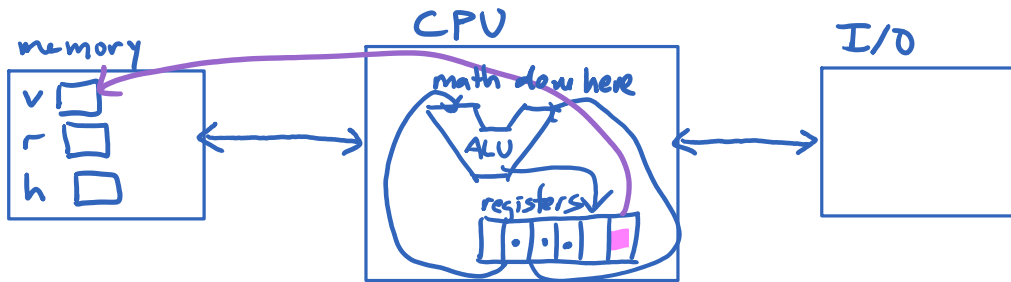


CPU: Qualcomm Snapdragon 845



CPU : Intel Core i5 6300U

von Neumann architecture



$$v = \pi * r * r * h$$

CPU 1

```

load r, r0
mult r0, r0, r1
load h, r2
mult r1, r2, r3
load 3.14159..., r4
mult r3, r4, r5
store r5, v
    
```

$r^2 \cdot h$
 $\pi \cdot r^2 \cdot h$

CPU2

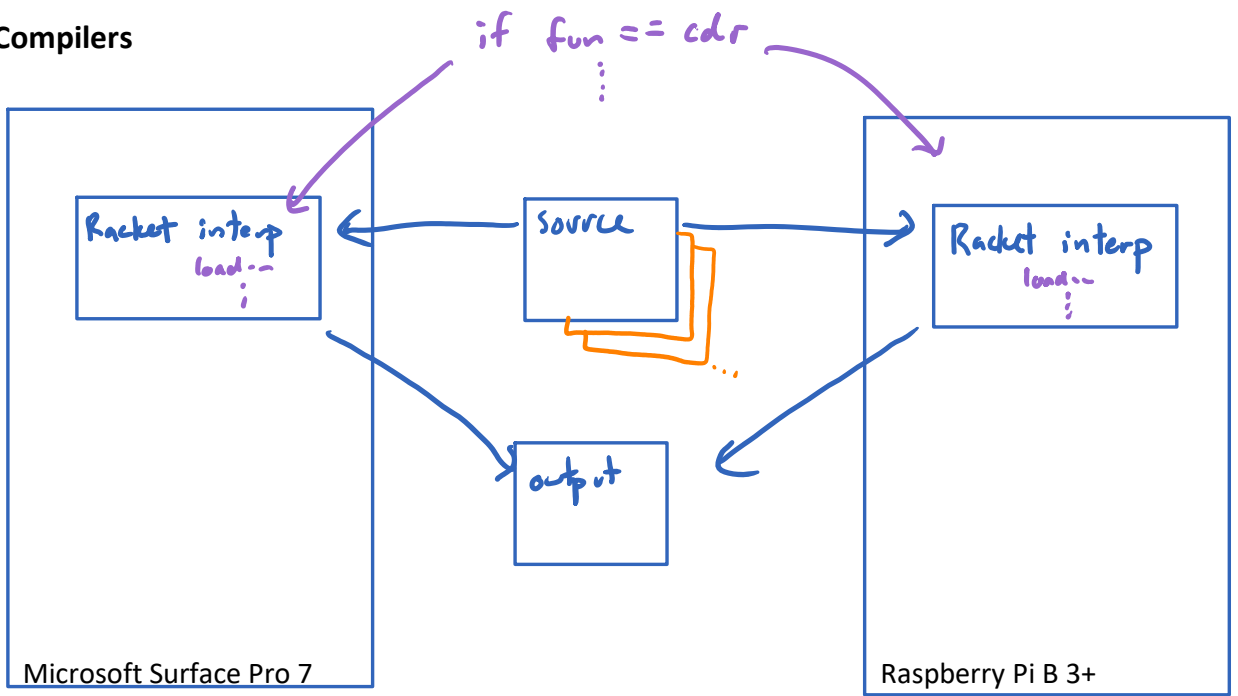
```

load r, r0
square r0
mult h, r0
mult-pi r0
store r0, v
    
```

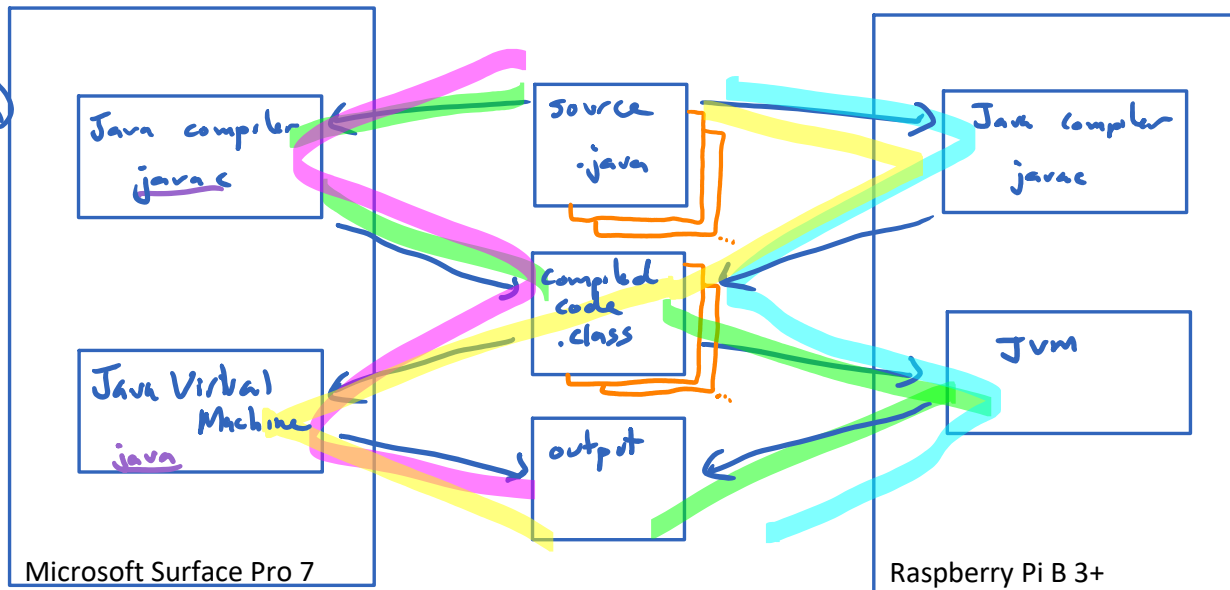
Interpreters vs Compilers

Interpreter

Racket



Java
C++
Python (implementation)



C

