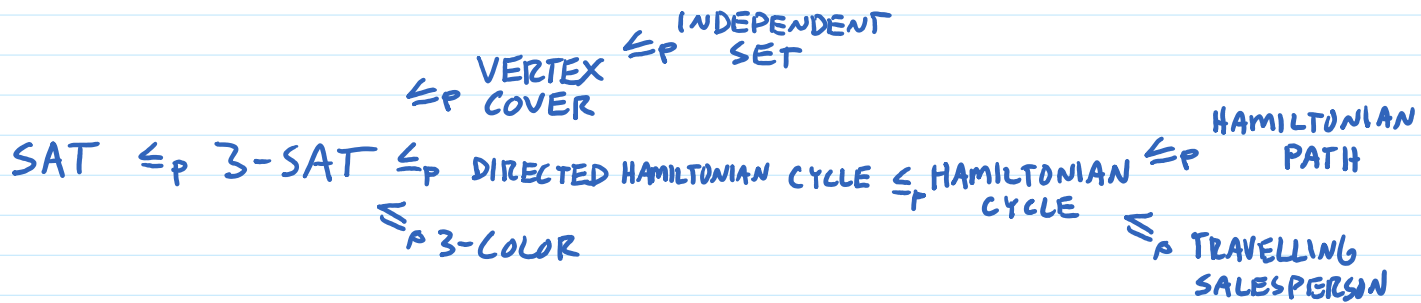


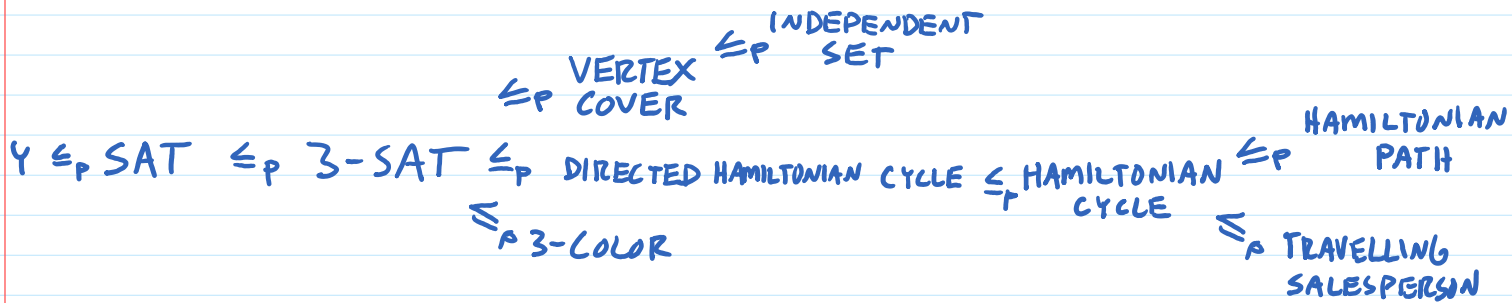
## Chain of Reductions

To show  $X$  is NP-complete : 1) show  $X \in NP$   
2) show  $Y \leq_p X$  for some NP-complete  $Y$



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Cook-Levin Theorem: SAT is NP-complete  
 $SAT \in NP$

for all  $Y \in NP$ ,  $Y \leq_p SAT$

## Reducing an arbitrary Y in NP to SAT

Let  $Y \in NP$ . Then there is a polynomial-time verification algorithm  $Y\text{-VERIFY}$

$$p \rightarrow q \equiv \neg p \vee q \qquad p \leftrightarrow q \equiv (p \rightarrow q) \wedge (q \rightarrow p)$$

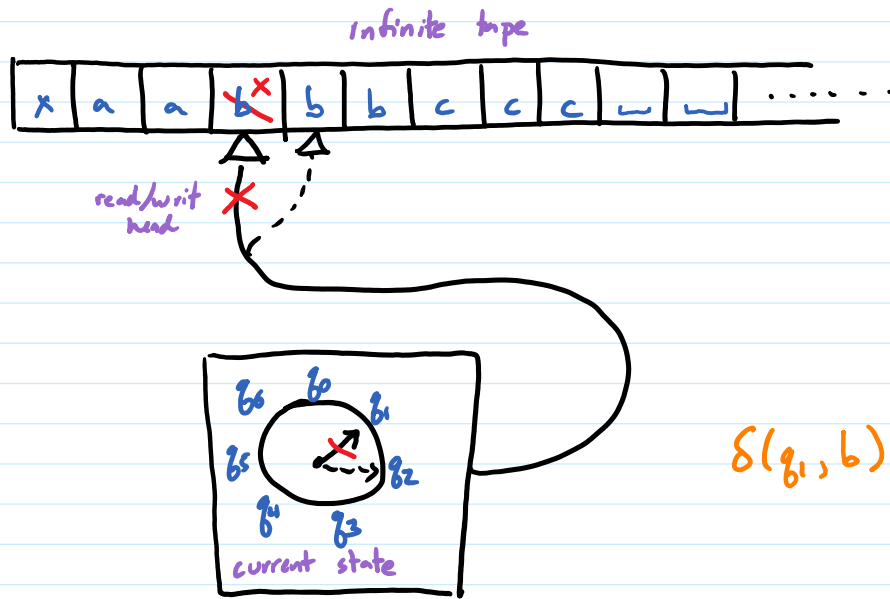
" $a_{t,i} = \sigma$ " meaning "location  $i$  at time  $t$  contains  $\sigma$ "  $\equiv \left( \bigwedge_{\sigma' \neq \sigma} \neg a_{t,i,\sigma'} \right) \wedge a_{t,i,\sigma}$

$b_t = q$  meaning "state is  $q$  at time  $t$ "

$c_t = i$  meaning "r/w head is at  $i$  at time  $t$ "

} similar

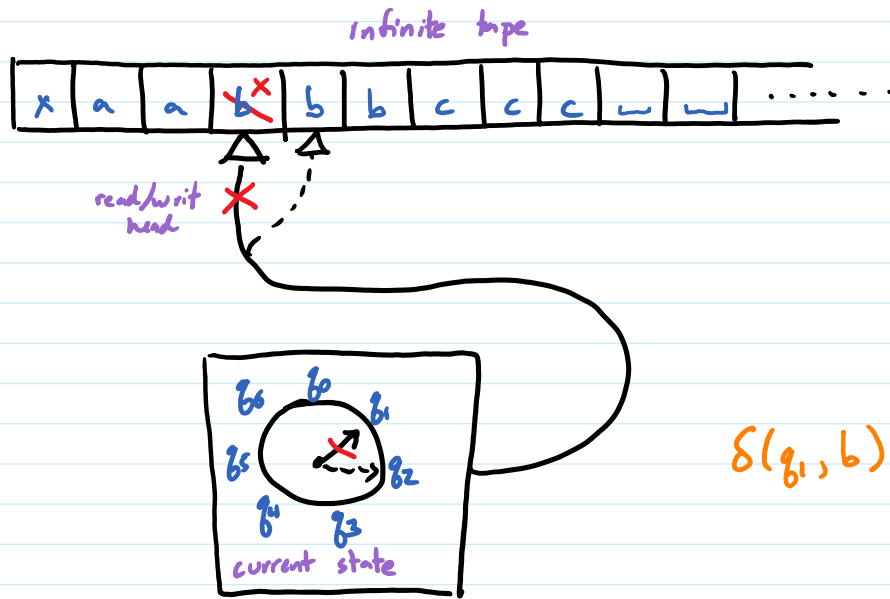
# Turing Machines



Polynomial-time algorithm in your favorite language  $\rightarrow$  polynomial time Turing machine

Proof: see CPSC 460

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[need to, given  $x$ , create a formula  $\varphi$  such that  $Y(x) = \text{YES} \iff \varphi$  is satisfiable  
and length of  $\varphi$  is polynomial in length of  $x$ ]

Let  $p(n)$  be such that Turing machine  $M$  for  $Y\text{-VERIFY}(x,y)$  takes  $p(|x|+|y|)$  time.

Let  $g(n)$  be such that  $\forall x$  s.t.  $Y(x) = \text{YES}$ ,  $\exists y$  s.t.  $|y| \leq g(|x|)$  and  $Y\text{-VERIFY}(x,y) = \text{YES}$   
total running time =  $p(|x|+|y|)$   
=  $p(n+g(n))$   
= polynomial in  $n$   
||  
|x|

$$p \rightarrow q \equiv \sim p \vee q$$

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total running time =  $p(|x|+|y|)$   
 $= p(n+g(n))$   
 $= \text{polynomial in } n$   
 call this  $r(n)$

Interpret  $a_{t,i,\sigma} = T$  to mean tape location  $i$  contains symbol  $\sigma$  at time  $t$

$b_{t,q} = T$  to mean  $M$  is in state  $q$  at time  $t$

$c_{t,i} = T$  to mean the read/write head is at location  $i$  at time  $t$

polynomial  $\left[ \begin{array}{l} 0 \leq t \leq r(|x|) \\ 0 \leq i \leq r(|x|) \end{array} \right. \left. \begin{array}{l} q \in \{0, \dots, k-1\} \\ \sigma \in \Sigma \cup \{\_, \textcircled{P}, \textcircled{N}\} \end{array} \right]$  finite so polynomial # of variables

$$p \rightarrow q \equiv \neg p \vee q$$

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Huge Formula

From  $x = x_0 \dots x_{n-1}$  create  $\varphi$  to express

note: omitting edge cases  
(end of tape, M halts, ...)

each tape location contains exactly one symbol at any time

$$\bigwedge_{t=0}^{r(n)} \bigwedge_{i=0}^{r(n)} \bigvee_{\sigma \in \Sigma \cup \{ \emptyset, \ominus \}} (a_{t,i,\sigma} \wedge \bigwedge_{\sigma' \neq \sigma} \neg a_{t,i,\sigma'})$$

M is in exactly one state and the read/write head is at exactly one location at any time  
similar

1<sup>st</sup> input to M is  $x$ , the tape after the 2<sup>nd</sup> input is blank and M starts in state  $Q_0$ , location  $0$

$$\bigwedge_{i=0}^{n-1} a_{0,i,x_i}$$

$$\bigwedge_{i=n+q(n)}^{r(n)} a_{0,i,\ominus}$$

$$b_{0,0}$$

$$c_{0,0}$$

at all times, the tape, state, and head position reflect the operation of M

$$\bigwedge_{t=0}^{r(n)} \bigwedge_{i=0}^{r(n)} \bigwedge_{\sigma \in \Sigma \cup \{ \emptyset, \ominus \}} [ (\neg c_{t,i} \wedge a_{t,i,\sigma}) \rightarrow a_{t+1,i,\sigma} ]$$

Ex:  $\delta(q, \sigma) = (q', \sigma', R)$

(tape doesn't change where read/write head isn't)

$$\bigwedge_{t=0}^{r(n)} \bigwedge_{i=0}^{r(n)} \left[ \begin{array}{c} (c_{t,i} \wedge b_{t,q} \wedge a_{t,i,\sigma}) \\ \downarrow \\ (c_{t+1,i+1} \wedge b_{t+1,q'} \wedge a_{t+1,i,\sigma'}) \end{array} \right]$$

the output is YES

$$\bigvee_{i=0}^{r(n)} \bigvee_{j=0}^{r(n)} a_{i,j,\ominus}$$

polynomial # of terms    constant size per term    so conjunction of all parts is of size polynomial in size of  $x$

$$Y(x) = \text{YES} \rightarrow \exists \text{ some } y \text{ s.t. } Y\text{-VERIFY}(x,y) = M(x,y) = \text{YES}$$

setting variables in  $\varphi$  according to input  $x,y$ , and behavior of machine makes  $\varphi$  true

$Y(x) = \text{NO} \rightarrow$  no  $y$  makes  $Y\text{-VERIFY}(x,y) = M(x,y) = \text{YES} \rightarrow$  all truth assignments make  $\varphi$  false  
(either no tape location is ever  $\ominus$  or the variables don't reflect the operation of M)