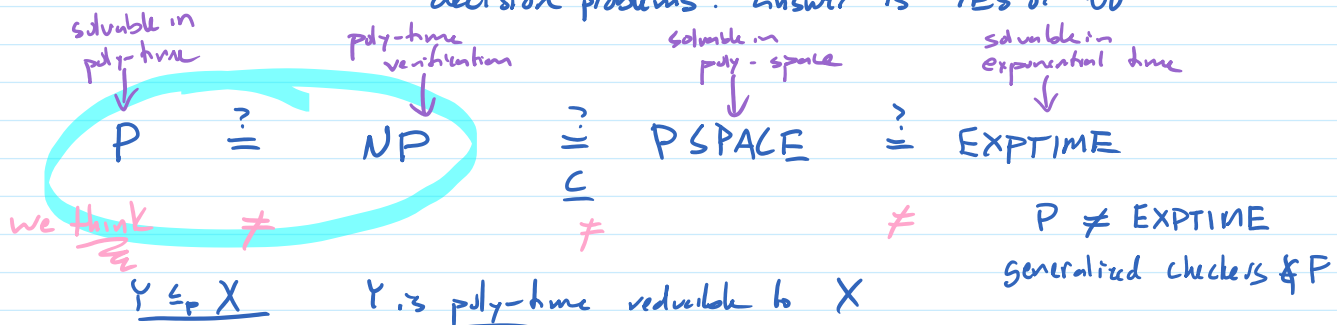


Summary/Roadmap

decision problems: answer is YES or NO



Let $A \in NP$ and $A \notin P$ then $P \neq NP$ and all NP-complete problems are not in P

let B be NP-complete

Suppose $TSP \in P$ then $P = NP$

$A \in NP$ $A \leq_p TSP$ $A \in P$

$A \leq_p B$ $B \in P$

Polynomial-time verification algorithm for decision problem X .

For all x , $X(x) = \text{YES} \iff$ there is a y such that $X\text{-VERIFY}(x,y) = \text{YES}$

\rightarrow given G , does there exist a Hamilton cycle in G

HC-VERIFY(G, p) \leftarrow polynomial in size of G

poly
poly
poly

check that each v in G appears at least once in p
 check that each v in G appears at most once in p
 for each $(v_i, v_j) \in p$, check that edge (v_i, v_j) exists in G
 and edge from last to first exists in G
 if all YES, return YES
 else return NO

$HC(G) = \text{YES}$

if G has Hamiltonian cycle c , then HC-VERIFY(G, c) = YES

if G has no Hamiltonian cycle, then HC-VERIFY(G, p) = NO for all p
 $HC(G) = \text{NO}$

NP = set of problems X s.t. there is a polynomial-time verification algorithm for X

NP' = set of problems X s.t. there is a non-deterministic polynomial-time solution for X
 allow coin flips

solution for X
 $X(x) = \text{YES}$
 \downarrow
 $P(X\text{-RANDOM}(x) = \text{YES}) > 0$

$HC \in \text{NP}'$:

HC-RANDOM(G)

guess

check

\rightarrow randomly permute vertices to get v_0, \dots, v_{n-1}, v_0
 for $i=0$ to $n-1$
 if no edge $(v_i, v_{(i+1) \% n})$ output NO
 output YES

if G has HC c , then with prob $\frac{1}{n!} > 0$ we choose c and output YES

if G has no HC, output NO no matter what we choose in 1st step

NP = NP'

$\text{NP} \subseteq \text{NP}'$: Let $X \in \text{NP}$. Then \exists poly-time verifier $X\text{-VERIFY}(x,y)$

Write non-deterministic alg for X : X-RANDOM(x)
 randomly generate y
 output $X\text{-VERIFY}(x,y)$

$X(x) = \text{YES} \rightarrow \exists y$ s.t. $X\text{-VERIFY}(x,y) = \text{YES} \rightarrow X\text{-RANDOM}$ picks y with prob > 0
 $\rightarrow X\text{-RANDOM}$ outputs YES w/ prob > 0

$X(x) = NO \rightarrow \forall y, X\text{-VERIFY}(x, y) = NO \rightarrow X\text{-RANDOM}(x)$ outputs NO always
 $\rightarrow X\text{-RANDOM}(x)$ outputs YES w/prob 0

$NP' \in NP$: Let $x \in NP'$. Then \exists poly-time non-deterministic $X\text{-RANDOM}(x)$.

Write verifier $X\text{-VERIFY}(x, y)$
simulate $X\text{-RANDOM}(x)$ using y as random bits

$X(x) = YES \rightarrow P(X\text{-RANDOM}(x) = YES) > 0$
 \rightarrow some sequence of random bits y makes $X\text{-RANDOM}(x) = YES$
 $\rightarrow X\text{-VERIFY}(x, y) = YES$

$X(x) = NO \rightarrow P(X\text{-RANDOM}(x) = NO) = 0$
 \rightarrow all y make $X\text{-VERIFY}(x, y) = NO$

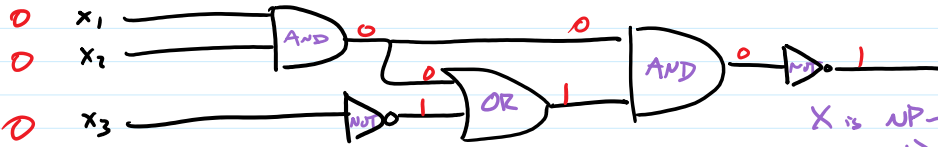
CIRCUIT-SAT and SAT

$CIRCUIT-SAT \leq_p SAT \leq_p 3-SAT \leq_p VC \leq_p HC$

SAT \in NP (done in video) } \rightarrow so SAT is NP-complete

CIRCUIT-SAT \leq_p SAT - given φ , determine if φ has a satisfying assignment

\hookrightarrow given combinational circuit C , is there an input to make output 1



X is NP-complete means
 1) $X \in NP$
 2) for all $Y \in NP, Y \leq_p X$

CIRCUIT-SAT is NP-complete

want: CIRCUIT-SAT(C)

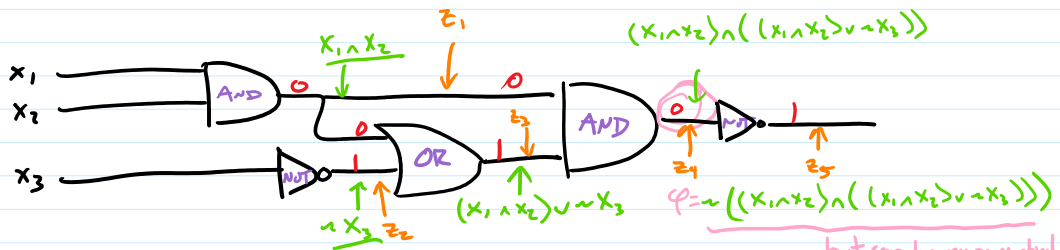
poly create φ from C \leftarrow φ has a satisfying assignment
 output SAT(φ) \leftarrow C has a satisfying input

To show X is NP-complete

- 1) show $X \in NP$
- 2) choose NP-complete Z
- show $Z \leq_p X$

if $SAT \in P \rightarrow CIRCUIT-SAT \in P$
 $CIRCUIT-SAT \notin P \rightarrow SAT \notin P$

for all $Y \in NP, Y \leq_p Z \leq_p X$
 \sum so $Y \leq_p X$



1 subexpression for each gate, each of $O(1)$ size



$$\varphi = z_5 \wedge (z_5 \leftrightarrow \neg z_4) \wedge (z_4 \leftrightarrow (z_1 \wedge z_2)) \wedge (z_3 \leftrightarrow (z_1 \vee z_2)) \wedge (z_2 \leftrightarrow \neg x_3) \wedge (z_1 \leftrightarrow (x_1 \wedge x_2))$$

φ is satisfiable $\iff C$ is satisfiable

size is linear in size of circuit

SAT and 3-SAT

3-SAT ∈ NP.

SAT ≤_P 3-SAT

so 3-SAT is NP-complete

Given ϕ in 3CNF

$(x \vee y \vee z) \wedge (\neg x \vee y \vee z) \wedge (x \vee x \vee x) \wedge \dots$

want: given ϕ , find 3CNF ϕ'

sat. ϕ is satisfiable $\Leftrightarrow \phi'$ is satisfiable

$(x \wedge y) \wedge \neg(x \vee y)$
 $\uparrow \quad \uparrow \quad \uparrow$
 $z_1 \quad z_2 \quad z_3$

same idea as in CIRCUIT-SAT ≤_P SAT

$\phi' = z_4 \wedge (z_4 \leftrightarrow (z_1 \wedge z_3)) \wedge (z_3 \leftrightarrow \neg z_2)$
 $\wedge (z_2 \leftrightarrow x \vee y)$
 $\wedge (z_1 \leftrightarrow x \wedge y)$

x	y	z ₁	z ₁ ↔ x ∧ y
T	T	T	T
T	T	F	F
T	F	T	F
T	F	F	T
F	T	T	F
F	T	F	T
F	F	T	F
F	F	F	T

now this is $\sim \wedge \sim \wedge \sim \wedge \sim$
 but each \sim is not (t_1, v, t_2, v, t_3)
 so use truth tables to convert

one clause for each T row in truth table

$z_1 \leftrightarrow x \wedge y \equiv (x \wedge y \wedge z_1) \vee (x \wedge \neg y \wedge \neg z_1) \vee (\neg x \wedge y \wedge \neg z_1) \vee (\neg x \wedge \neg y \wedge z_1)$

$(x \wedge y \wedge \neg z_1) \vee (x \wedge \neg y \wedge z_1) \vee (\neg x \wedge y \wedge z_1) \vee (\neg x \wedge \neg y \wedge \neg z_1)$

close... now is $(\sim \wedge \sim \wedge \sim) \vee (\sim \wedge \sim \wedge \sim) \vee \dots$

wanted $(\sim \vee \sim \vee \sim) \wedge (\sim \vee \sim \vee \sim) \wedge \dots$

try again

$z_1 \leftrightarrow x \wedge y \equiv \sim \sim (z_1 \leftrightarrow x \wedge y) \equiv \sim ((x \wedge y \wedge \neg z_1) \vee (x \wedge \neg y \wedge z_1) \vee (\neg x \wedge y \wedge z_1) \vee (\neg x \wedge \neg y \wedge \neg z_1))$

get this from the F rows in truth table

$\equiv (\neg x \vee \neg y \vee z_1) \wedge (\neg x \vee y \vee \neg z_1) \wedge (x \vee \neg y \vee \neg z_1) \wedge (x \vee y \vee z_1)$

De Morgan

this is 3CNF; call it ϕ_1

$\phi' = z_4 \wedge \phi_4 \wedge \phi_3 \wedge \phi_2 \wedge \phi_1$

conjunction of 3CNF is still 3CNF

each ϕ_i is $O(1)$ size and can be found in $O(1)$ time

no matter how big ϕ is, each truth table has only 8 rows

so ϕ' is linear in # of operators in ϕ = linear in size of ϕ
 and can be created in poly time