CPSC 467: Cryptography and Computer Security

Instructor: Michael Fischer
Lecture by Ewa Syta

Lecture 5a
September 11, 2013

Advanced Encryption Standard

AES Alternatives

Advanced Encryption Standard

New Standard

Rijndael was the winner of NIST's competition for a new symmetric key block cipher to replace DES.
An open call for algorithms was made in 1997 and in 2001 NIST announced that AES was approved as FIPS PUB 197.

Minimum requirements:

- Block size of 128-bits
- Key sizes of 128-, 192-, and 256-bits
- Strength at the level of triple DES
- Better performance than triple DES
- Available royalty-free worldwide

Five AES finalists:

- MARS, RC6, Rijndael, Serpent, and Twofish

Details

Rijndael was developed by two Belgian cryptographers Vincent Rijmen and Joan Daemen.

Rijndael is pronounced like *Reign Dahl*, *Rain Doll* or *Rhine Dahl*.

Name confusion

- AES is the name of the standard.
- Rijndael is the name of the cipher.
- AES is a restricted version of Rijndael which was designed to handle additional block sizes and key lengths.

More details

AES was a replacement for DES.

- Like DES, AES is an iterated block cipher.
- Unlike DES, AES is not a Feistel cipher.
- Unlike DES, AES can be parameterized.

AES supports key lengths of 128-, 192- and 256-bits.

The algorithm consists of 10 to 14 rounds.

- Number of rounds depends on the key length.
- 10 rounds for 128-bit key, 12 for 192, 14 for 256.

How does AES actually work?

3 Big Ideas:

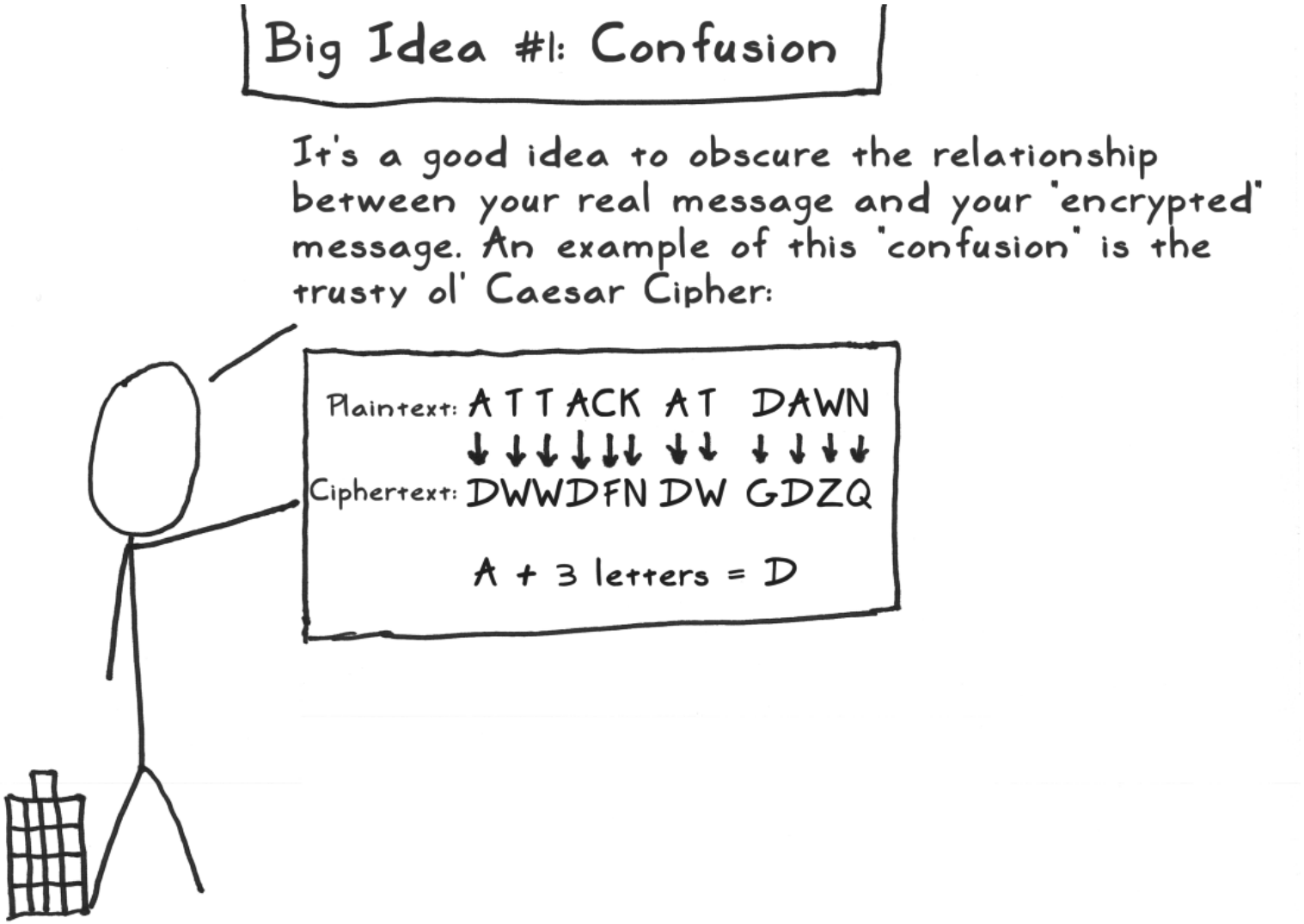
- Big Idea #1: Confusion
- Big Idea #2: Diffusion
- Big Idea #3: Key secrecy

Confusion & Diffusion

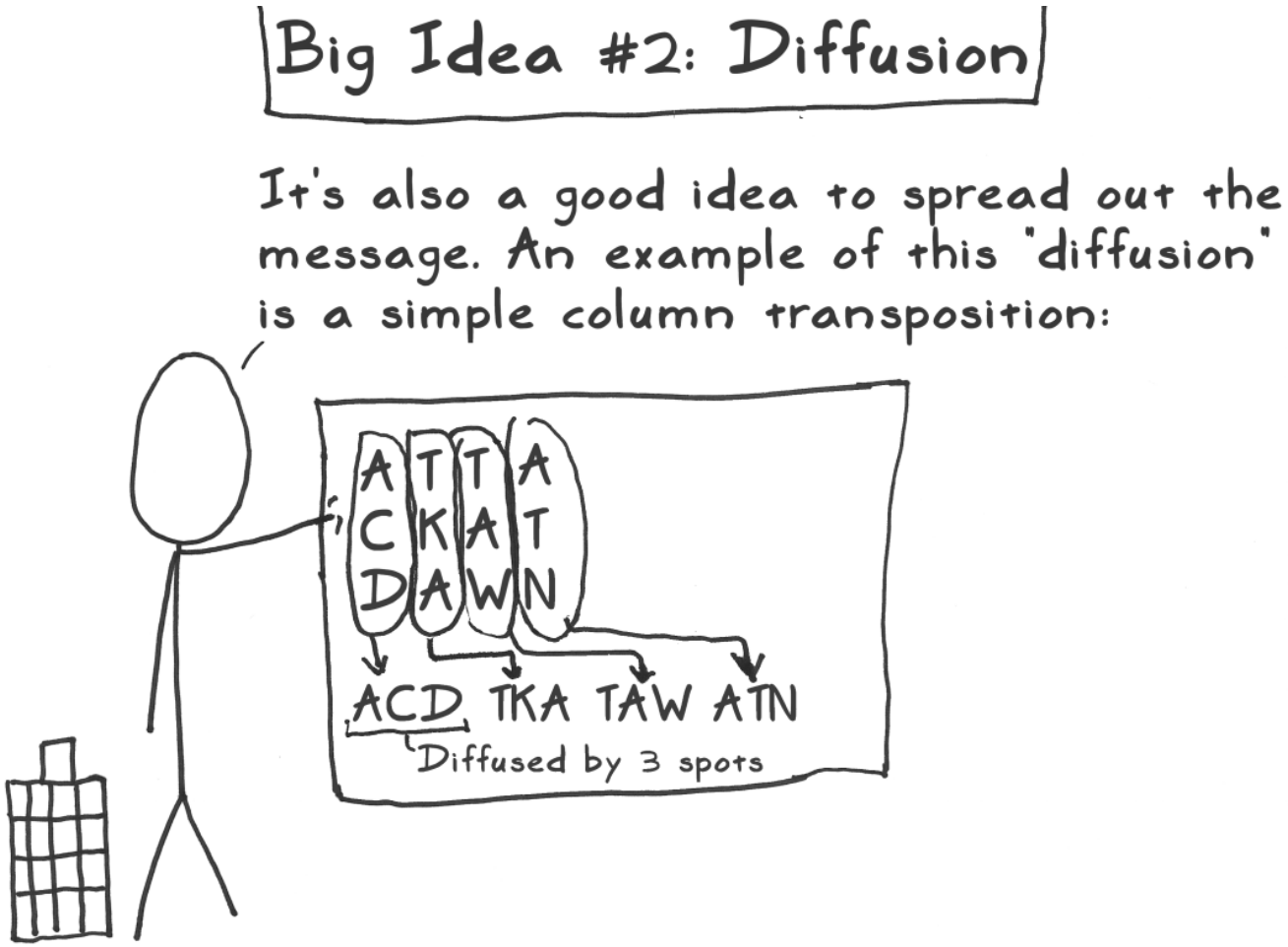
Confusion and *diffusion* are two properties of the operation of a secure cipher which were identified by Claude Shannon in his paper *Communication Theory of Secrecy Systems*.

`http://netlab.cs.ucla.edu/wiki/files/shannon1949.pdf`

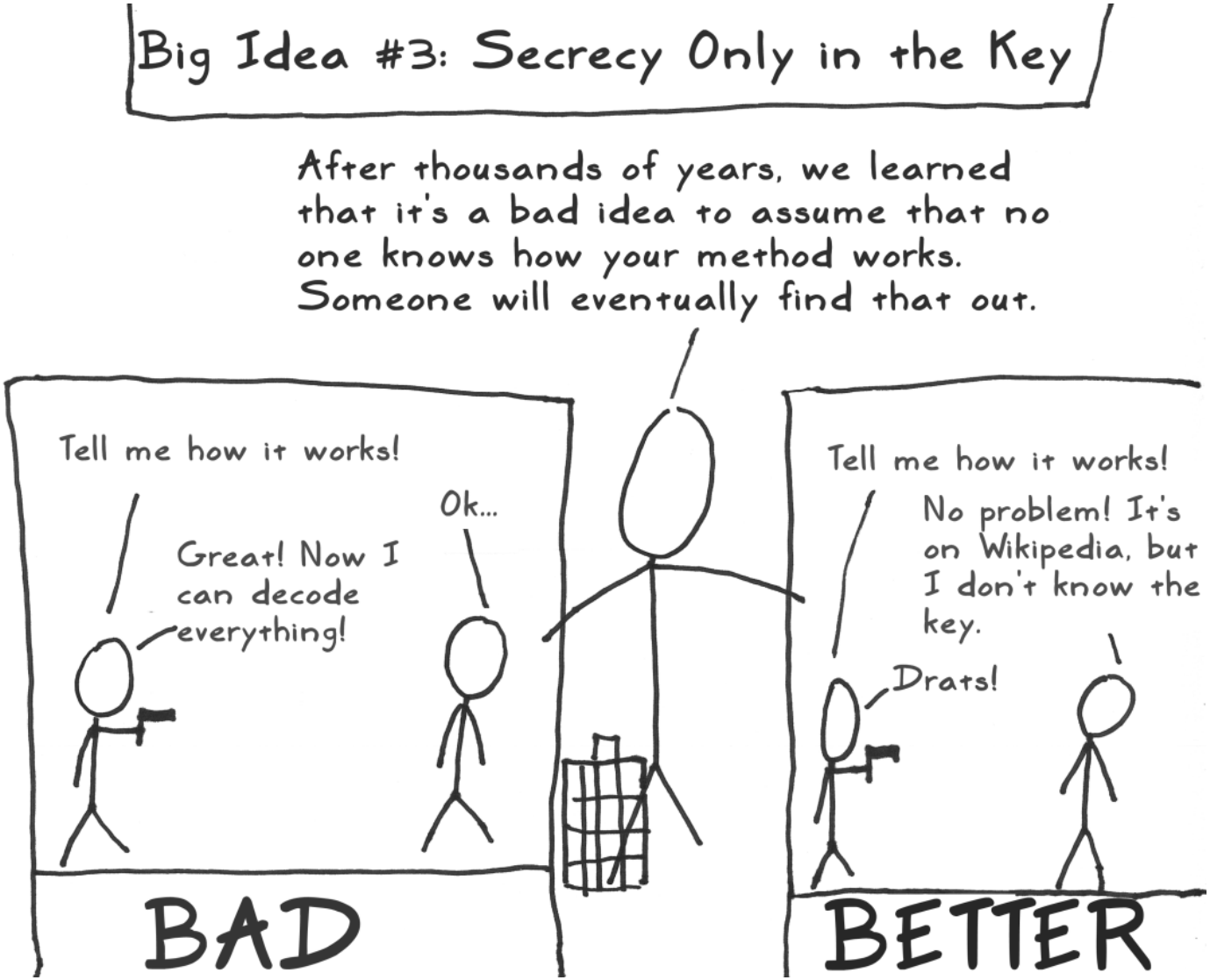
DES, AES and many block ciphers are designed using Shannon's idea of confusion and diffusion.



www.moserware.com/2009/09/stick-figure-guide-to-advanced.html



www.moserware.com/2009/09/stick-figure-guide-to-advanced.html



www.moserware.com/2009/09/stick-figure-guide-to-advanced.html

Transformations

Each AES round consists of 4 transformations:

- SubBytes(State)
- ShiftRows(State)
- MixColumns(State)
- AddRoundKey(State, Key)

Each round works on the state array.

A round key is derived from the primary key using a key schedule algorithm.

All four transformations are invertible.

Roles of the four transformations

SubBytes() replaces bytes using a fixed S-box to achieve non-linearity.

ShiftRow() and *MixColumns()* are intended to mix up bits to achieve a wider distribution of plaintext in the whole message space.

AddRoundKey() provides the necessary secret randomness.

How do these transformations relate to the Big Ideas?

Roles of the four transformations

Big Idea #1 *SubBytes()* replaces bytes using a fixed S-box to achieve non-linearity.

Big Idea #2 *ShiftRow()* and *MixColumns()* are intended to mix up bits to achieve a wider distribution of plaintext in the whole message space.

Big Idea #3 *AddRoundKey()* provides the necessary secret randomness.

Preliminaries

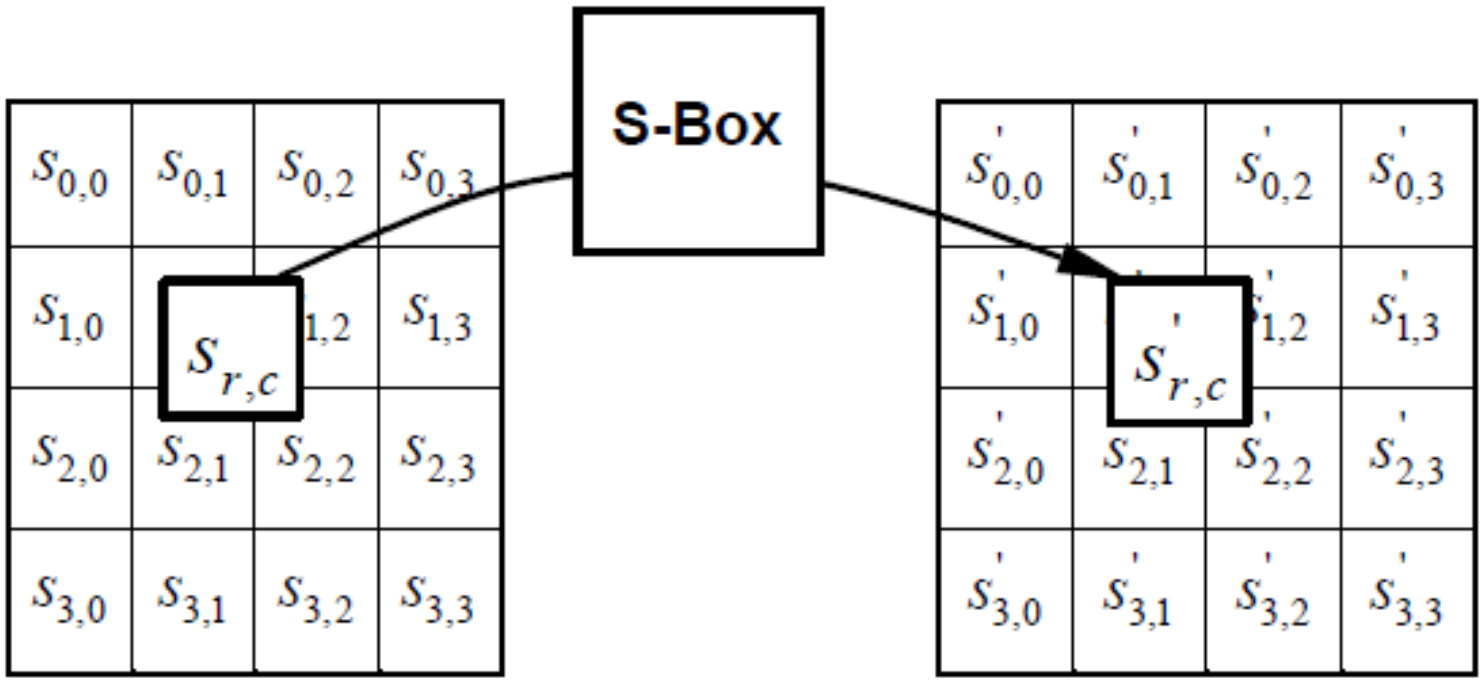
We will consider the minimum case of 128-bit key.

- The input and output arrays consist of sequences of 128 bits represented by a 4 x 4 matrix of 8-bit bytes.
- The intermediate state is referred to as the state array.
- Columns and rows are also referred to as words which consist of 4 bytes.

All AES images come from FIPS Pub 197 available at
`http://csrc.nist.gov/publications/fips/fips197/fips-197.pdf`

SubBytes()

Non-linear byte substitution that operates independently on each byte of the state array using a substitution table (S-box).



SubBytes() S-box

Example: SubBytes(45) = 6e

- Rows: First 4 bits of the input byte
- Columns: Last 4 bits of input

		y															
		0	1	2	3	4	5	6	7	8	9	a	b	c	d	e	f
x	0	63	7c	77	7b	f2	6b	6f	c5	30	01	67	2b	fe	d7	ab	76
	1	ca	82	c9	7d	fa	59	47	f0	ad	d4	a2	af	9c	a4	72	c0
	2	b7	fd	93	26	36	3f	f7	cc	34	a5	e5	f1	71	d8	31	15
	3	04	c7	23	c3	18	96	05	9a	07	12	80	e2	eb	27	b2	75
	4	09	83	2c	1a	1b	6e	5a	a0	52	3b	d6	b3	29	e3	2f	84
	5	53	d1	00	ed	20	fc	b1	5b	6a	cb	be	39	4a	4c	58	cf
	6	d0	ef	aa	fb	43	4d	33	85	45	f9	02	7f	50	3c	9f	a8
	7	51	a3	40	8f	92	9d	38	f5	bc	b6	da	21	10	ff	f3	d2
	8	cd	0c	13	ec	5f	97	44	17	c4	a7	7e	3d	64	5d	19	73
	9	60	81	4f	dc	22	2a	90	88	46	ee	b8	14	de	5e	0b	db
	a	e0	32	3a	0a	49	06	24	5c	c2	d3	ac	62	91	95	e4	79
	b	e7	c8	37	6d	8d	d5	4e	a9	6c	56	f4	ea	65	7a	ae	08
	c	ba	78	25	2e	1c	a6	b4	c6	e8	dd	74	1f	4b	bd	8b	8a
	d	70	3e	b5	66	48	03	f6	0e	61	35	57	b9	86	c1	1d	9e
	e	e1	f8	98	11	69	d9	8e	94	9b	1e	87	e9	ce	55	28	df
	f	8c	a1	89	0d	bf	e6	42	68	41	99	2d	0f	b0	54	bb	16

SubBytes()

Each non-zero byte x is substituted using the following transformation $y = Ax^{-1} + b$

- If x is a zero byte, then $y = b$

$$A = \begin{pmatrix} 1 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 1 & 1 & 0 & 0 & 0 & 1 & 1 & 1 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 1 \\ 1 & 1 & 1 & 1 & 0 & 0 & 0 & 1 \\ 1 & 1 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 & 1 \end{pmatrix} \qquad b = \begin{pmatrix} 1 \\ 1 \\ 0 \\ 0 \\ 0 \\ 1 \\ 1 \\ 0 \end{pmatrix}$$

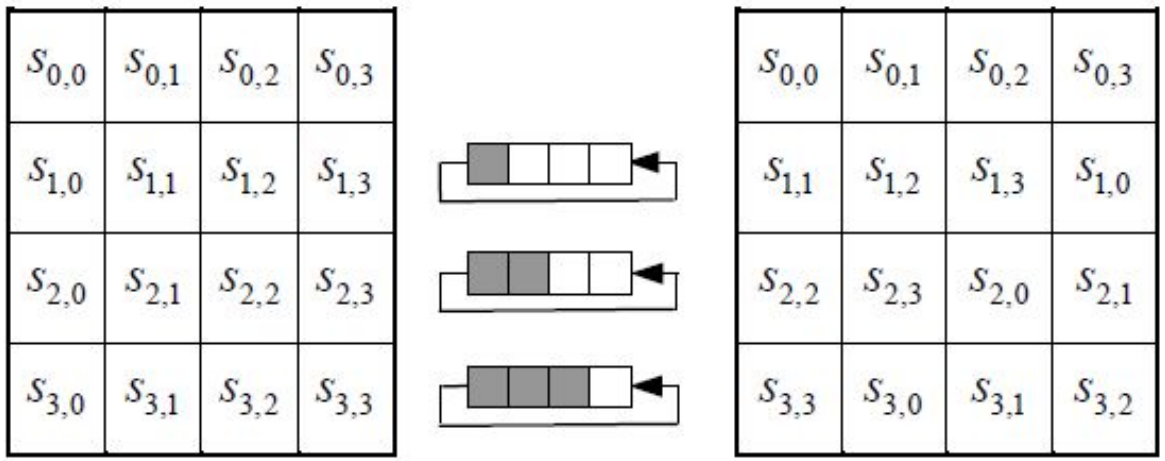
S-box is just a pre-computed table of inverses. It eliminates the possibility of a timing analysis attack:

- Observing the time difference may give out whether an operation is performed on a zero or a non-zero byte.

ShiftRows()

The bytes are cyclically shifted over by 0, 1, 2 and 3 bytes.

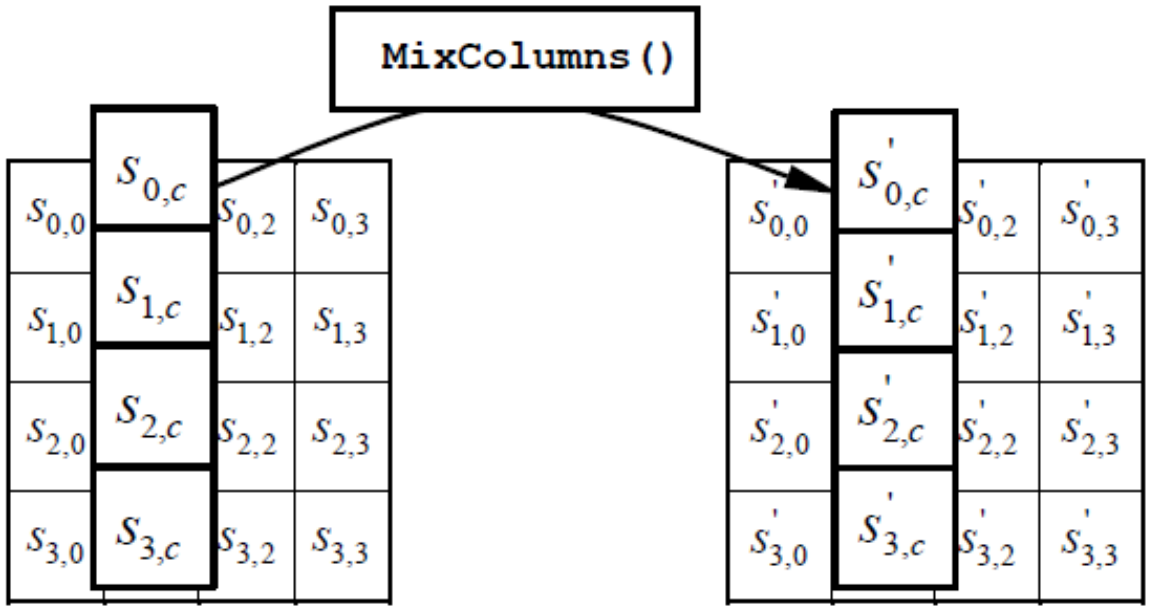
This operation works like a transposition cipher because only the positions of bytes are changed, not the bytes themselves.



MixColumns()

Operates on the state array column-by-column.

Each column is multiplied by a fixed array.



Matrix multiplication

$$\begin{bmatrix} s'_{0,c} \\ s'_{1,c} \\ s'_{2,c} \\ s'_{3,c} \end{bmatrix} = \begin{bmatrix} 02 & 03 & 01 & 01 \\ 01 & 02 & 03 & 01 \\ 01 & 01 & 02 & 03 \\ 03 & 01 & 01 & 02 \end{bmatrix} \begin{bmatrix} s_{0,c} \\ s_{1,c} \\ s_{2,c} \\ s_{3,c} \end{bmatrix}$$

As a result of this multiplication, the four bytes in a column are replaced by the following:

$$s'_{0,c} = (\{02\} \bullet s_{0,c}) \oplus (\{03\} \bullet s_{1,c}) \oplus s_{2,c} \oplus s_{3,c}$$

$$s'_{1,c} = s_{0,c} \oplus (\{02\} \bullet s_{1,c}) \oplus (\{03\} \bullet s_{2,c}) \oplus s_{3,c}$$

$$s'_{2,c} = s_{0,c} \oplus s_{1,c} \oplus (\{02\} \bullet s_{2,c}) \oplus (\{03\} \bullet s_{3,c})$$

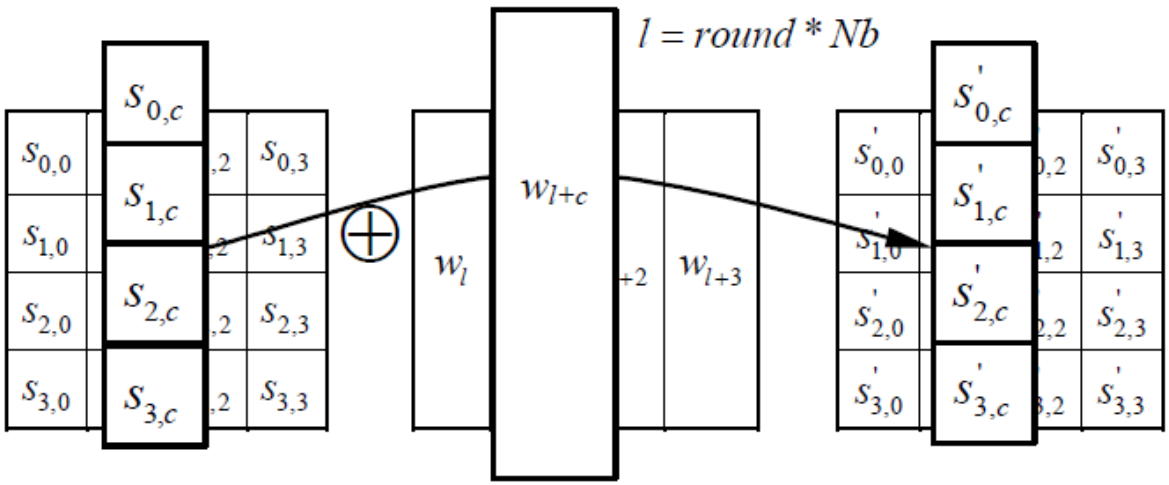
$$s'_{3,c} = (\{03\} \bullet s_{0,c}) \oplus s_{1,c} \oplus s_{2,c} \oplus (\{02\} \bullet s_{3,c}).$$

$\oplus$ exclusive OR operation, $\bullet$ finite field multiplication

AddRoundKey()

Each column of the state array is XORed with a word from the key schedule.

The round key is determined by the key schedule algorithm.



Nb - number of columns, here Nb = 4

Decryption

AES is not a Fiestel cipher so decryption works differently than encryption. Steps are done in reverse.

All four transformations are invertible

- InvShiftRows() - bytes in the last three rows of the state array are cyclically shifted over to the right
- InvSubBytes() - the inverse S-box is applied to each byte of the state array
- InvMixColumns() - the state array is multiplied by the matrix inverse used in MixColumns()
- AddRoundKey() is its own inverse, since it is an XOR operation

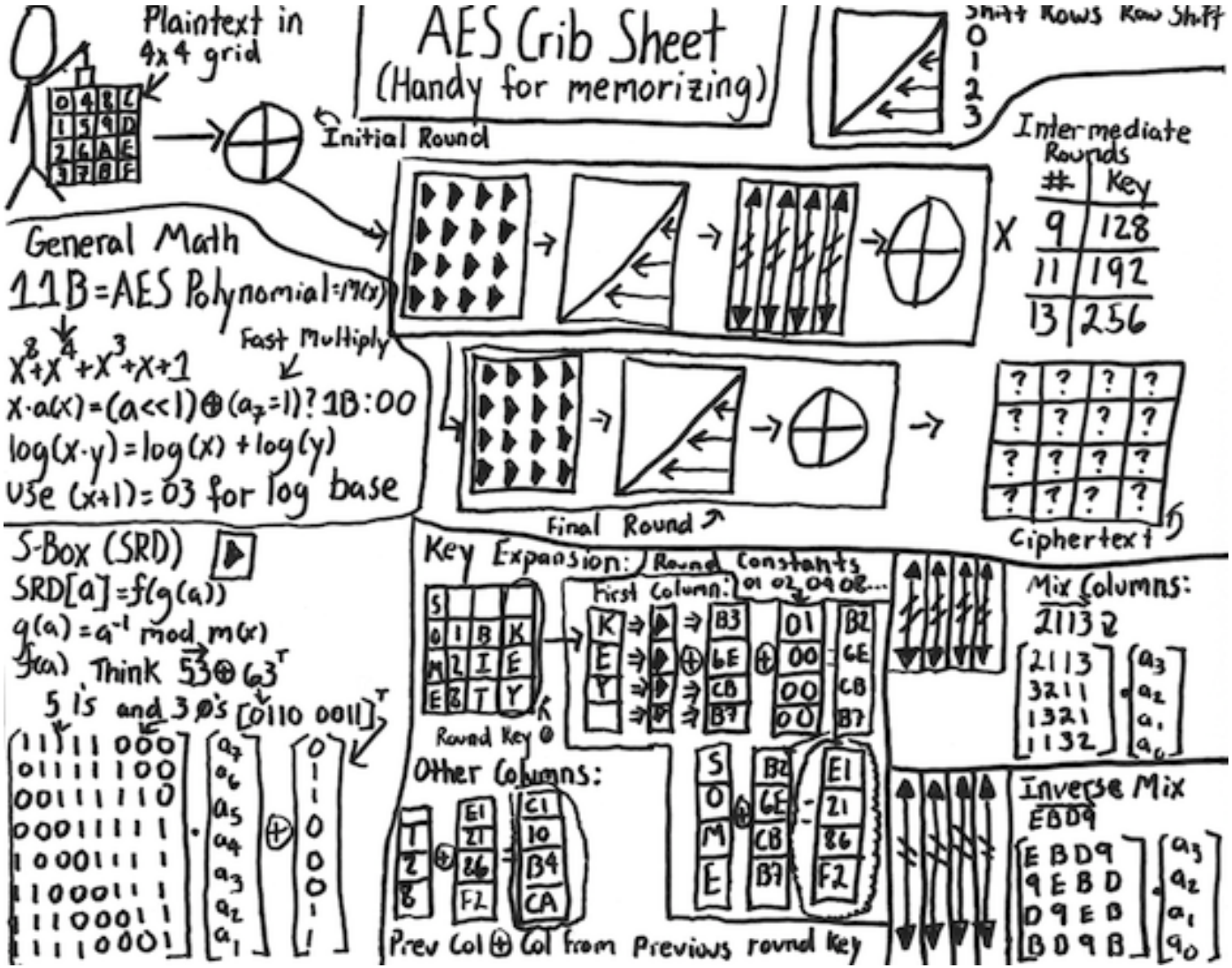
Encryption

- ARK
- BS, SR, MC, ARK
- ...
- BS, SR, MC, ARK
- BS, SR, ARK

Decryption

- ARK, ISR, IBS
- ARK, IMC, ISR, IBS
- ...
- ARK, IMC, ISR, IBS
- ARK

MixColumns() is not applied in the last round in order to make the encryption and decryption more similar in structure. This is similar to the absence of the swap operation in the last round of the DES.



www.moserware.com/2009/09/stick-figure-guide-to-advanced.html

Hardware Support for AES

Both Intel and AMD provide a set of instructions for AES promising 3x to 10x acceleration versus pure software implementation.

- `AESENC/AESDEC` - one round of encryption / decryption
- `AESENCLAST/AESDECLAST` - last round of encryption / decryption
- `AESKEYGENASSIST` - key expansion

Breaking AES

The ability to recover a key from known or chosen ciphertext(s) with a reasonable time and memory requirements.

Frequently, reported attacks are attacks on the implementation, not the actual cipher:

- Buggy implementation of the cipher (e.g., memory leakage)
- Side channel attacks (e.g., time and power consumption analysis, electromagnetic leaks)
- Weak key generation (e.g., bad PRBGs, attacks on master passwords)
- Key leakage (e.g., a key saved to a hard drive)

AES Security

All known attacks are computationally infeasible.

Interesting results:

- Best key recovery attack: AES-128 with computational complexity $2^{126.1}$, AES-192 - $2^{189.7}$, and AES-256 - $2^{254.4}$

  A. Bogdanov, D. Khovratovich and C. Rechberger, *Biclique Cryptanalysis of the Full AES*, ASIACRYPT 2011

- Related-key attack on AES-256 with complexity 2^{99} given 2^{99} plaintext/ciphertext pairs encrypted with four related keys.

  A. Biryuko and D. Khovratovich, *Related-key Cryptanalysis of the Full AES-192 and AES-256*, ASIACRYPT 2009

Bruce Schneier on AES security

"I don't think there's any danger of a **practical** attack against AES for a long time now. Which is why the community should start thinking about migrating now" (2011)

"Cryptography is all about **safety margins**. If you can break n round of a cipher, you design it with $2n$ or $3n$ rounds. At this point, I suggest AES-128 at 16 rounds, AES-192 at 20 rounds, and AES-256 at 28 rounds. Or maybe even more; we don't want to be revising the standard again and again" (2009)

Bruce Schneier's Blog

`http://www.schneier.com/blog/archives/2011/08/new_attack_on_a_1.html`

AES Alternatives

Other ciphers

There are many good block ciphers to choose from:

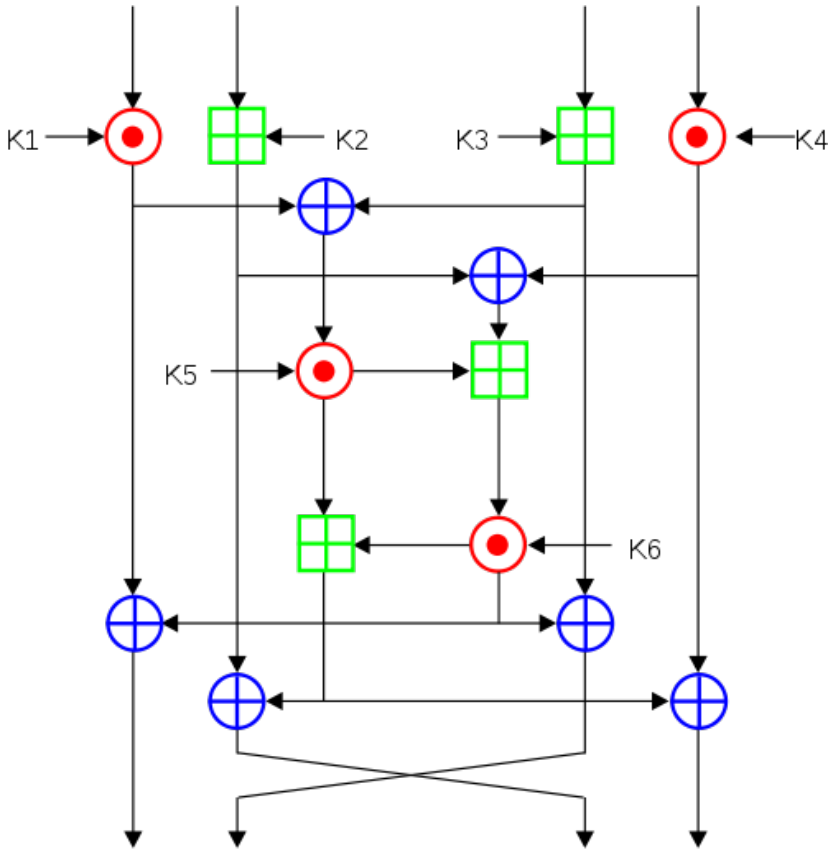
- Blowfish, Serpent, Twofish, Camellia, CAST-128, IDEA, RC2/RC5/RC6, SEED, Skipjack, TEA, XTEA

We will have a brief look at

- IDEA
- Blowfish
- RC6
- TEA

IDEA (International Data Encryption Algorithm)

- Invented by James Massey
- Supports 64-bit data block and 128-bit key
- 8 rounds
- Novelty: Uses mixed-mode arithmetic to produce non-linearity
  - Addition mod 2 combined with addition mod 2^{16}
  - Lai-Massey multiplication ˜multiplication mod 2^{16}
- No explicit S-boxes required



multiplication modulo $2^{16} + 1$ bitwise XOR addition modulo 2^{16}

Image retrieved from `http://en.wikipedia.org/wiki/International_Data_Encryption_Algorithm`

Blowfish

- Invented by Bruce Schneier
- Supports 64-bit data block and a variable key length up to 448 bits
- 16 rounds
- Round function uses 4 S-boxes which map 8 bits to 32 bits
- Novelty: the S-boxes are key-dependent (determined each time by the key)

RC6

- Invented by Ron Rivest
- Variable block size, key length, and number of rounds
- Compliant with the AES competition requirements (AES finalist)
- Novelty: data dependent rotations
  - Very unusual to rely on data

TEA (Tiny Encryption Algorithm)

- Invented by David Wheeler and Roger Needham
- Supports 64-bit data block and 128-bit key
- Variable number of rounds (64 rounds suggested)
  - "Weak" round function, hence large number of rounds
- Novelty: extremely simple, efficient and easy to implement

TEA Encryption(32 rounds)

$(K[0], K[1], K[2], K[3]) =$ 128 bit key
$(L, R) =$ plaintext (64-bit block)
delta $= 0$x9e3779b9
sum $= 0$
for $i = 1$ to 32
    sum $=$ sum $+$ delta
    $L = L + (((R \ll 4) + K[0]) \oplus (R + \text{sum}) \oplus ((R \gg 5) + K[1]))$
    $R = R + (((L \ll 4) + K[2]) \oplus (L + \text{sum}) \oplus ((L \gg 5) + K[3]))$
next i
ciphertext $= (L, R)$

Tea Decryption

$(K[0], K[1], K[2], K[3]) =$ 128 bit key
$(L, R) =$ ciphertext (64-bit block)
delta $= 0$x9e3779b9
sum $=$ delta $\ll 5$
for $i = 1$ to 32
    $R = R - (((L \ll 4) + K[2]) \oplus (L + \text{sum}) \oplus ((L \gg 5) + K[3]))$
    $L = L - (((R \ll 4) + K[0]) \oplus (R + \text{sum}) \oplus ((R \gg 5) + K[1]))$
    sum $=$ sum $-$ delta
next i
plaintext $= (L, R)$

Figures retrieved from *Information Security Principles and Practice*, Mark Stamp, Wiley, 2006

Additional Resources

A Stick Figure Guide to AES by Jeff Moser Highly recommended!

`http://www.moserware.com/2009/09/stick-figure-guide-to-advanced.html`

AES Inspector by Enrique Zabala

`http://www.formaestudio.com/rijndaelinspector/archivos/inspector.html`

AES Animation by Enrique Zabala

`http://www.formaestudio.com/rijndaelinspector/archivos/rijndaelanimation.html`

AES Example by instructors at Massey U., New Zealand

`http://www.box.net/shared/static/uqrq0hmnb9.pdf`